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responsive to the currents supplied. The indicator circuit is responsive to changes in the summing node voltage level and generates at an output a logical signal at one state when the summing node voltage level is greater than a predetermined value and generates the logical signal at the output at another state when the summing node voltage level is less than the predetermined value, the predetermined value corresponding to a preselected power supply voltage.

Page 4, line 8, after "summing node.", delete "the current sources also are";

line 8, after "summing node", insert - 1, wherein the voltage at the summing node is responsive to the current supplied to the summing node

line 9, delete "connected to a power supply voltage,

wherein the current at";

line 10, de/ete "the summing node voltage is equal to zero when the power supply";

line 11, before "The indicator", delete "voltage is equal to a reference voltage."; and

line 14, /after "to" insert --voltage--.

Page 7, line 2 /after "current sources" delete "generating currents" and insert --each of which generates a current--; line 2, after "representing" insert --one of--;

line 3,/after "bandgap" delete "circuit" and insert -equation -; and

line 10, after "kT/q" delete "maybe" and insert -- are--.

Page 8, after VSUM." delete "if the currents do not sum or add up to zero at the node VSUM, the" and insert -- The--;

line 4, after "saturation" delete "on" and insert -corresponding to-;

line 5, before "current mirrors" insert --mirror or--; line 5, after larger current" insert --or currents--.

Page 12, line 11, after "as a result, the" delete "some of the currents at" and insert --voltage level of--;

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